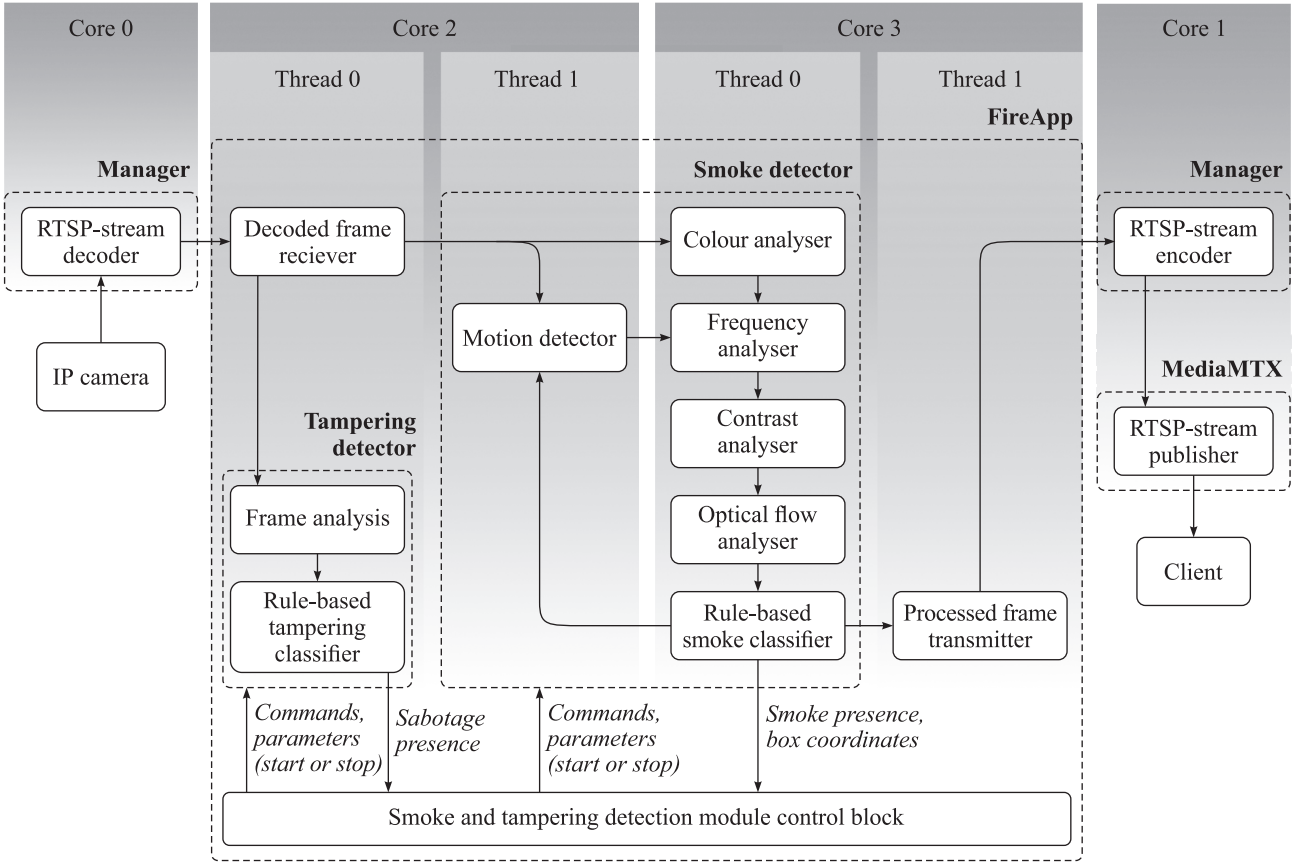

Fig. 2. Block diagram of the distribution of tasks in a model based on a 4-core CPU SBC