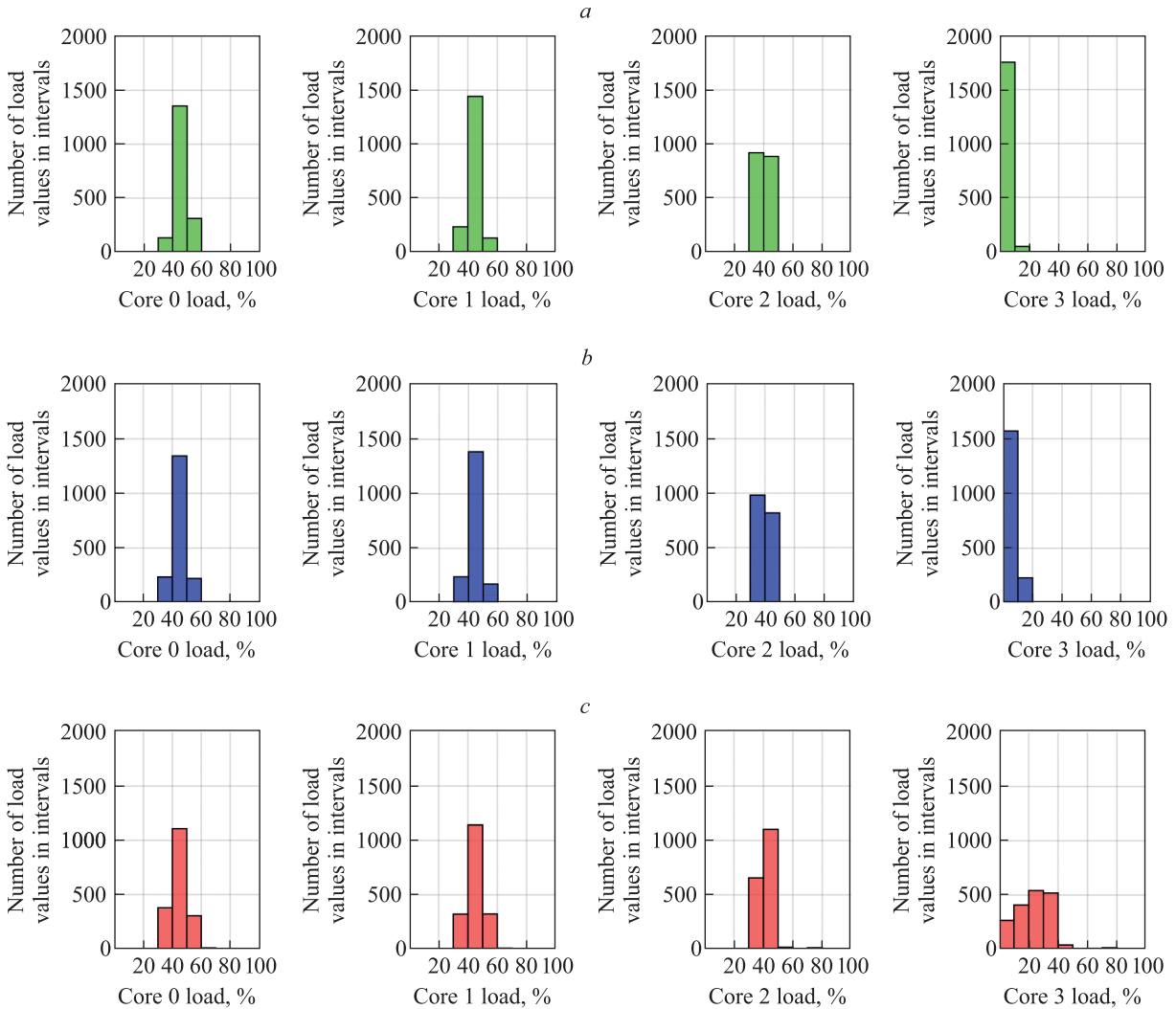

Fig. 7. RPi4B CPU core load: *a* – mode 1; *b* – mode 2; *c* – mode 3